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(54) Data transmission to a mass audience

(57) A method of data assembly for distribution of the data via a mass audience distribution system 50, comprises storing in a random access memory 38 packets of data 36 which are to be transmitted to the audience, storing in a first address table 42 a sequence of memory addresses of the data for transmission of the data in the sequence, progressively reading at 44 the first table to retrieve the addresses of the sequence, reading the memory 38 to retrieve the packets of data 36 in the sequence, and transmitting the read packets to the audience in real time. Broadcast parameter data are stored in a second table 39 in a RAM 40 and a processor 32 reads table 39 and constructs table 42. Table 39 includes pointers to the address in RAM 38 where packets 36 start.

The data is combined with video signals at 48 for cable TV broadcasts to subscribers 51.

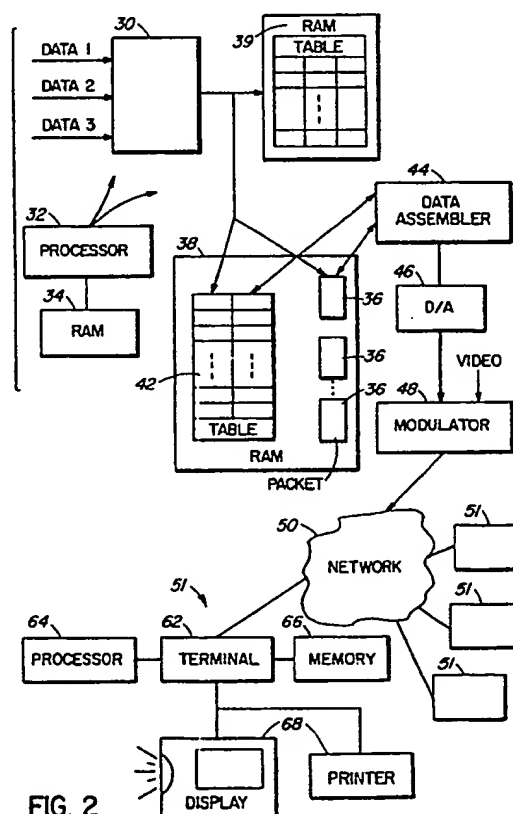


FIG. 2

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At least one drawing originally filed was informal and the print reproduced here is taken from a later filed formal copy.

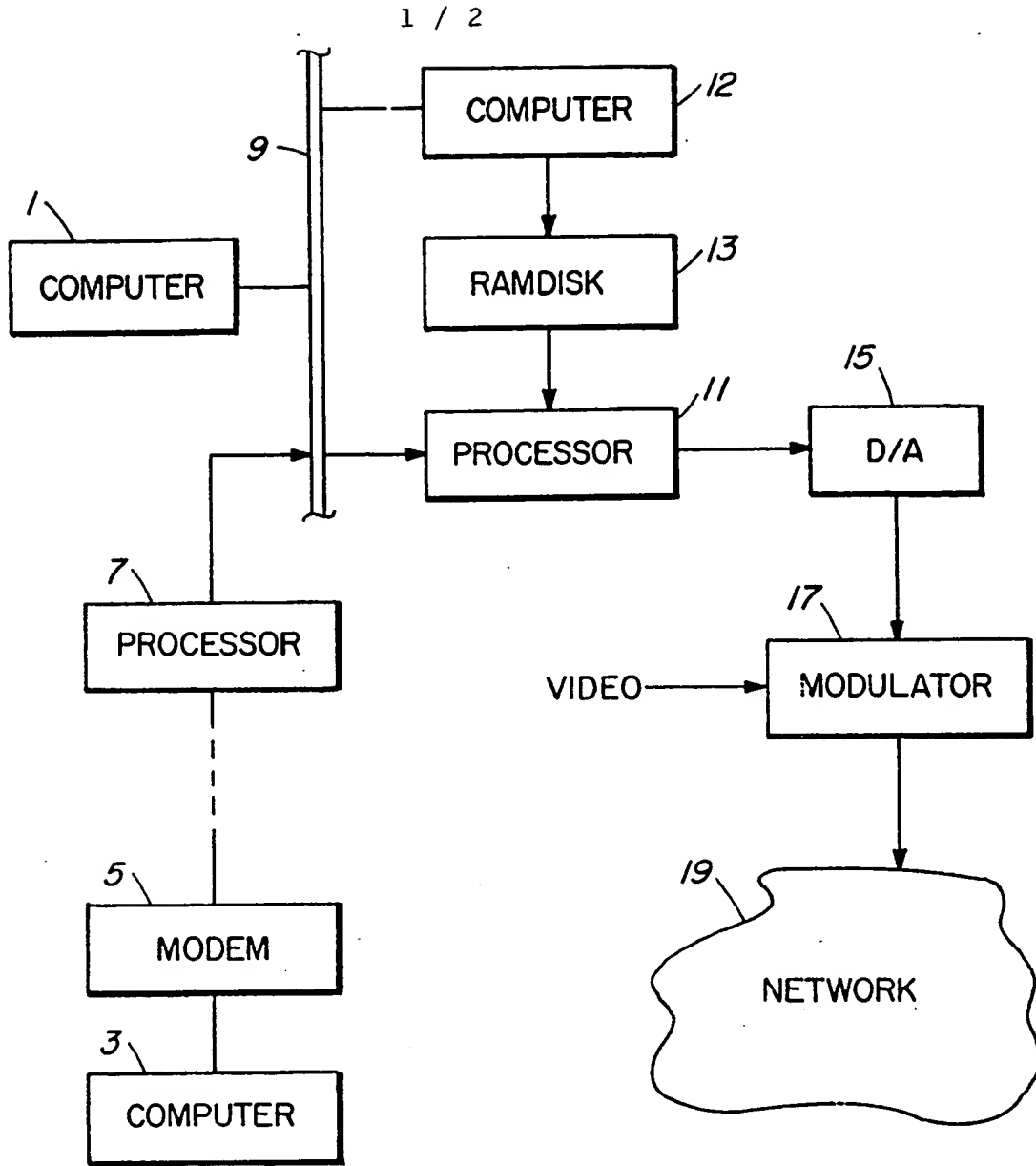


FIG. 1

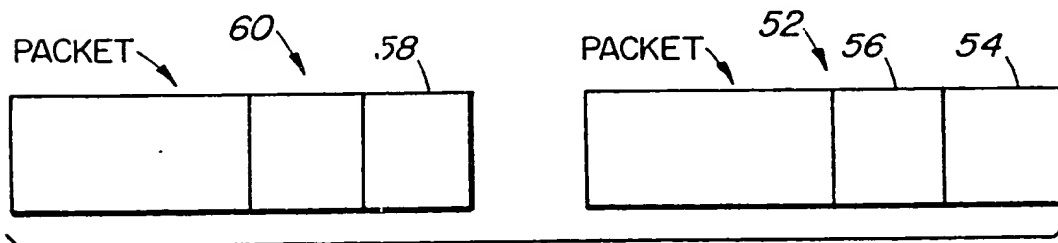


FIG. 3

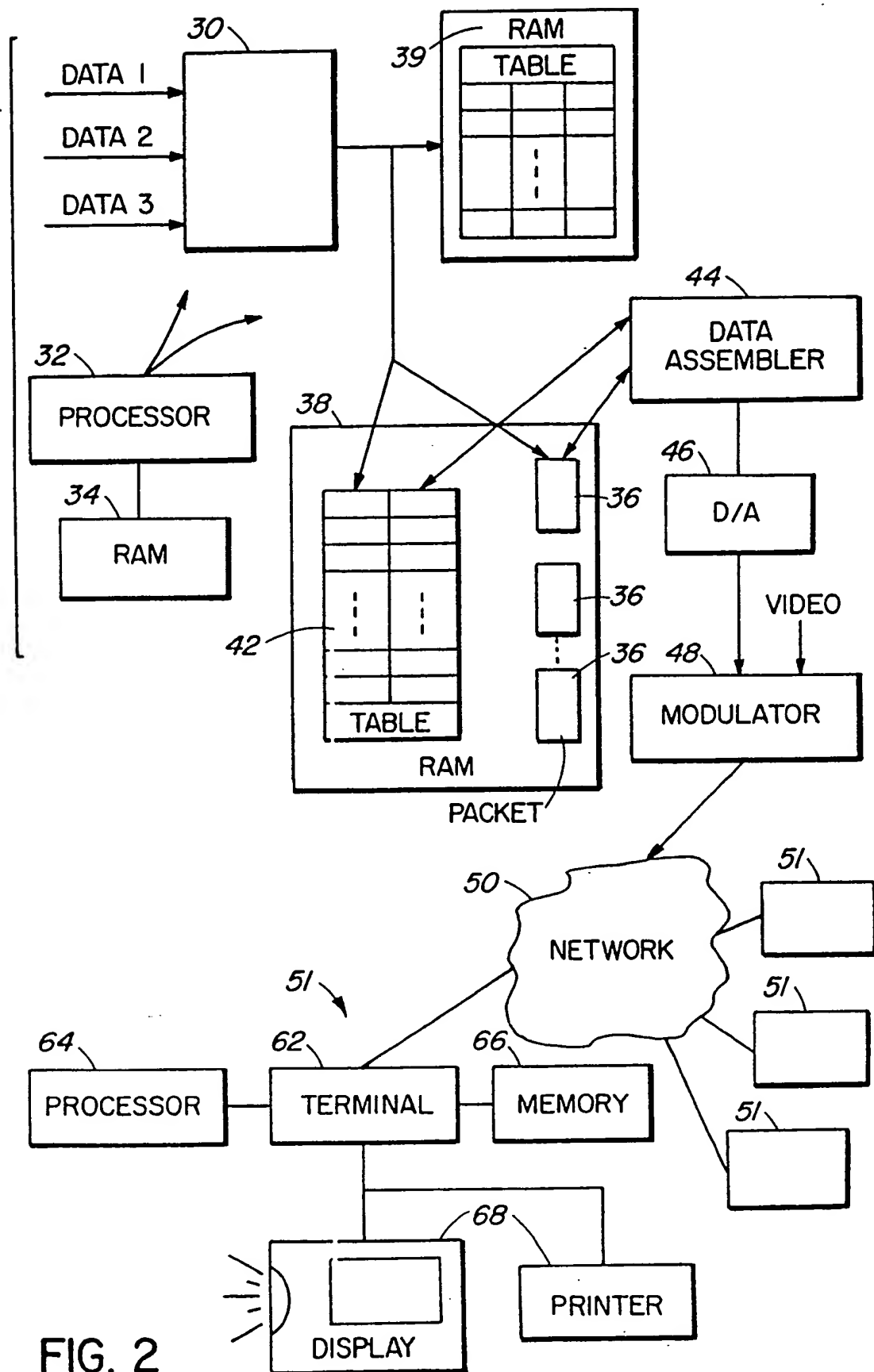


FIG. 2

FIELD OF THE INVENTION

This invention relates to a method of data transmission to a mass audience, which is particularly useful for transmitting data to subscribers via a cable TV network or other electronic mass distribution network.

BACKGROUND TO THE INVENTION

Cable TV and other types of electronic mass audience distribution systems such as broadcasting by domestic wireless television stations or via satellite have distributed more than only television signals in recent years. For example, such systems transmit closed caption text for the hard of hearing, that can be decoded by a closed caption decoder, they transmit descrambling codes to be received by pay TV customers of a cable TV system operator, videotext is transmitted and displayed on a TV screen as text or simple graphics files, they download games from a head end which can be played via a TV set, etc. In more recent years, various other services are contemplated or have begun, such as banking, narrowcasting, electronic mail, facsimile broadcast or narrowcasting to individual subscriber stations, etc., all carried via an electronic mass distribution system.

The data which is transmitted typically has several characteristics in common. The data carries a header which contains a fixed or variable address of a particular recipient. In case the recipient is one of a specialized audience group, the header contains a common address of the group. The data is typically transmitted either during the 21st scanning line of a television channel, from where it is decoded by a decoder at the subscriber location, or it is sent in a separate data channel, and is decoded from the data channel at the subscriber location. One system of this kind that

broadcasts data is described in U.S. Patent 4,623,920, dated November 18th, 1986, invented by Michel Dufresne et al, and entitled "Cable Network Data Transmission System".

5 Data to be transmitted is assembled at a head end in a RAMDISK, which is cyclically read in a sequential manner. The data that is read is interspersed with null data, in order to give receiver processors, which are considerably less expensive than
10 the head end processor and thus are much slower in operation, time to process the received data. Null data is also inserted in order to allow a head end processor to insert specialized data into the data stream, such as electronic mail, which is to be transmitted only once to
15 a single subscriber's terminal, in contrast to other data such as program announcements that is transmitted cyclically.

A block diagram of a system of the kind described above is shown in Figure 1. An information
20 supplier provides data signals which are stored on a hard disk drive of a minicomputer 1 such as a type PDP 11 at a head end. In addition a service provider, using e.g. an IBM compatible personal computer 3 provides data via a modem 5 and telephone line to a front end
25 processor 7. Both the processor 7 and computer 1 are connected in a network 9, such as Ethernet™, to which a main processor 11 is connected. Another personal computer 12 is connected to the network, and is also connected to a RAMDISK 13. RAMDISK 13 is also connected
30 to processor 11.

Computer 12 reads data from computer 1, and from processor 7 and writes it to the RAMDISK 13. Main processor 11 reads the data cyclically and sequentially from RAMDISK 13, and outputs it to digital to analog
35 converter 15, which converts the data to analog form and

presents it to modulator 17 for merging with video signals and subsequent transmission to a network 19 to which audience terminals have access.

It is possible for processor 7 to interrupt processor 11 and to cause processor 7 to insert data into the data stream. However, this data can only be inserted as it arrives on the network 9 (i.e. the data can be inserted once), and after the interrupt the processor 11 returns to the cyclic reading of the RAMDISK 13.

With the expected substantial increase in traffic to provide the aforementioned new services, the system shown in Figure 1 has exhibited substantial problems. A first problem is a limitation in capacity. In order to accommodate an increase in the amount of data to be sent in one cycle, the capacity of the RAMDISK mass storage device must become larger and larger. This increases the read cycle time of the RAMDISK, and slows down to an unacceptable degree the responsiveness of the system to, for example, commands sent upstream requesting a service, such as an update report on a bank balance, (for example, delays of 20 seconds to provide a response have been experienced), or the updating of closed captioning (which could unacceptably lag the accompanied story being shown on television).

Secondly, the cyclic operation requires the insertion of null data on the RAMDISK in order to accommodate the aforementioned insertion of data during the null data. The use of null data imposes an inherent limitation on the capacity of the system. No more insert data can be accommodated than the capacity of the null portions of the signals, and the more null data stored on the RAMDISK, the less cyclic data can be transmitted.

Thirdly, the above-described system cannot prioritize data signals that are to be sent on a cyclic basis. For example, if an emergency signal is to be broadcast on a cyclic schedule, it must be written to the RAMDISK. This takes time. If the reading of the RAMDISK has just passed the memory location where the emergency signal has just been written, it could take the reading time of the entire RAMDISK (e.g. 20 seconds for a large system) before the emergency signal is read and transmitted. This could result in tragic consequences if the emergency is, for example, a missile alert, warning of a tidal wave or earthquake, etc.

Further, the above-described system cannot accommodate different data signals that are to be transmitted synchronously at different frequencies.

SUMMARY OF THE INVENTION

The present invention is a system which overcomes the above-described problems and limitations. The present invention can transmit signals on any cyclic or noncyclic schedule, without limitation as to the capacity of a RAMDISK, and can transmit a signal any predefined number of times.

In accordance with the present invention, data to be transmitted is assembled into packets for transmission in real time, "on the fly". Each packet has, in effect, its own defined cycle, and it is transmitted in accordance with that definition.

In accordance with an embodiment of the invention, a method of data distribution to an audience is comprised of storing packets of data in a random access memory, storing a transmission characteristic for each packet, reading the characteristics and transmitting each packet to the audience in accordance with a frequency and timing depending on its particular characteristic.

In accordance with another embodiment, a method of data assembly for distribution of the data via a mass audience distribution system, is comprised of storing in a random access memory packets of data which
5 are to be transmitted to the audience, storing in a first address table a sequence of memory addresses of the data for transmission of the data in the sequence, progressively reading the first table to retrieve the addresses of those packets of data that are to be
10 transmitted to the audience in the sequence, reading the memory to retrieve the packets of data in the sequence, and transmitting the packets read from the memory to the audience in real time.

In accordance with another embodiment, the
15 method includes the step of storing a second packet characteristic table which contains a record for each of the packets stored in the memory, each record including a priority field and a repetition period field, reading the second table at least once each interval of time
20 defining a packet, and updating the first table at least once each time the first table is read to specify the memory addresses in the first table in accordance with priority data stored in the priority field and repetition data stored in the repetition field of the
25 second table for each packet of data.

In accordance with another embodiment, the method includes reading incoming data, determining a required priority and repetition frequency, if any, of the incoming data, packetizing the incoming data if it
30 is not packetized, and in which the storing steps include storing packets of data in the random access memory, updating the second table with data records relating to each of said packets for which priority and repetition frequency were determined, and updating the

first table under control of the data stored in the second table.

BRIEF INTRODUCTION TO THE DRAWINGS

A better understanding of the invention will be obtained by a consideration of the detailed description below of a preferred embodiment, with reference to the following drawings, in which:

Figure 1 is a block diagram of a system in accordance with the prior art,

Figure 2 is a block diagram of a system in accordance with a preferred embodiment of this invention, and

Figure 3 is an illustration of packets stored in a memory of the invention.

DETAILED DESCRIPTION OF THE INVENTION

Data to be transmitted is received on data input lines DATA 1, DATA 2, DATA 3, etc. by a multiplexer 30, which is under control of a microprocessor 32. Microprocessor 32 reads the data and segregates the data into packets. The data that is read by microprocessor 32 has been prepared by an information (e.g. service) provider, which may be a privileged subscriber, providing data by telephone, voice mail, etc., to contain information relating to the broadcast parameters, such as priority, frequency of transmission. This can include a data payload which designates this information, or can be merely a "contents" byte or bytes which, upon reading by the processor, causes it to access a lookup table in an associated memory 39 which stores the broadcast parameters and can supply the parameters data when the processor recognizes particular "contents" bytes.

Upon processing the input data into packets, the processor 32 stores the packets 36 in random access memory (RAM) 38. The parameter data is stored in a

table 39 in RAM 40. The table in RAM 40 preferably stores, in a record for each packet, data values designating priority, a repetition period, linking data (indicating whether the packet to be broadcast is
5 related to another identified packet on a time frame basis), time span between the packet and the one identified by the linking data, the number of transmissions for the packet, and, if desired, an internal identifier of a channel number on which the
10 packet is to be broadcast. The record also stores in the table a pointer to the address in RAM 38 where the packet starts.

The processor 32, having established the data in the table of RAM 40, establishes from that data
15 another table 42 in RAM 38 which is a list of beginning or complete addresses of packets 36 which are to be transmitted in sequence. For example, if one packet is to be transmitted several times during the reading time of table 42 as designated by the parameters stored in
20 the record for that packet in table 39, the address of the beginning of that packet in RAM 38 is placed in table 42 several times, spaced by the spacing parameter indicated in the record for that packet in table 39.

Data assembler 44 reads each entry in table 42
25 in RAM 38 sequentially, accesses the packet addresses, and reads the packets at the designated addresses, transmitting the data to a digital to analog converter 46 or other device which converts the data packets into a form for transmission (e.g. a phase shift modulator).
30 The data is then sent to a master modulator 48, which inserts the data signals into a proper channel, frequency multiplexing it with video channels, and transmits the multiplexed signals over a medium 50 to subscribers stations 51.

It should be noted that since the channels are multiplexed together, different bandwidths can be provided. Since the packet data is transmitted in accordance with criteria based on the service provided by the packets, the criteria, as defined by the packet parameters, can define the bandwidths required and can control the multiplexing together of plural data channels to result in the various bandwidths required to provide the various services, within a logical or defined frequency channel. Indeed the criteria can be used to select particular logical channels for transmission of particular services, so that a subscriber will always tune his receiver to a particular channel to obtain e.g. a game service. The packet parameters can define the required bandwidth and transmit the game program and/or data down any data channel, or would multiplex data channels together, and present the game service to the subscriber terminal on a logical channel that the subscriber believes (due to his channel display) is the game channel.

While table 42 of RAM 38 is being read, processor 32 should assemble another table to replace table 42 as soon as it has been read. Or, rather than assembling a complete table to replace the entire table 42, once individual entries of table 42 have been read, substitute entries can be placed into their memory locations so that after the table has been read, it can be read from the beginning again and the subsequent reading will cause a different sequence of packets to be read into data assembler 44.

Of course, data assembler 44 can be a receiver of packets instead of a reader of packets in RAM 38, and a processor associated with RAM 38, even processor 32 if it is sufficiently fast enough and has enough time capacity, can read the packet addresses and the packet

data in table 42 and provide the packets in the sequence designated by the order of addresses in table 42 to the data assembler 44.

Processor 32 thus reads table 39 and
5 constructs table 42. It must be a fast enough processor to be able to read table 39 at a frequency of at least once per packet interval. A type 68030 or 68040 processor has been found to work adequately for in a prototype system.

10 In this invention, therefore, instead of assembling data as a cyclically read RAMDISK, data is assembled into a sequence from a table of pointers to data packets already assembled in RAM 38, and stored in no particular order other than memory locations chosen
15 by the writing processor. Once reading of the table has been exhausted, in one embodiment a new table is created and put into RAM, or, as described above, in another embodiment table entries already read can be replaced in preparation for the following progressive reading of the
20 table of pointers. Packets which are required to be read only once, or which have been read as many times as the parameters in table 39 have designated, can be written over with new packets when processor 32 creates new packets and stores them in memory 38.

25 It will be understood that the various random access memories that store the packets and the tables can be combined if desired in any convenient manner, and the various processors can be a single processor or plural processors which perform the various functions.
30 For instance several processors can be used, one to read the incoming data and determine the priority, frequency of transmission, etc., and create and write the packets to RAM 38, and write table 39. Another processor can read table 39 and create table 42. Another processor
35 can read table 42, read the packets 36 at the designated

addresses read from table 42, and provide them to data assembler 44, etc.

It is preferred that the packets themselves should contain a header that includes a reference to a following packet in a sequence. Thus for example as shown in Figure 3, packet 52 should contain in its header, not only an identifier byte 54 of that packet, but also a reference byte 56 to an identifier byte 58 of a following packet in sequence of meaningful data. Thus for example, there could be several intervening packets between packet 52 and packet 60 which carry data which have no content relationship to packets 52 and 60, and thus the reference byte 56 ties packets 52 and 60 together with regard to content. This allows a subscriber terminal 62 to receive the data, and under control of a local processor 64 detect the identifier bytes and reference bytes of successive packets, store them in a local memory 66, assemble related packets in sequence for transmission to a display 68 and/or audio amplifier for provision to a subscriber, since a preceding packet in effect carries a linking address of the next packet in line having related content.

The present invention can be more economical in memory and processor usage if table 42 merely stores the address of a first packet in a sequence, and not the addresses of other packets in the sequence. In that case once the first packet has been read for transmission to data assembler 44, the address in RAM 38 of the beginning of the following packet of the sequence can be read from the identifier 56 carried by the preceding packet. The packet at that address is read, and from its identifier byte 56 the address in RAM 38 of the next packet in the sequence can be determined, and the packet at that address read, and so on, until a null identifier byte is read. At that point the controlling

processor can read the next record in table 42 to obtain the address of the packet in the next sequence to be sent.

Since the present invention assembles data for
5 transmission on a real time basis, creates a sequence of data that has no cyclic relationship to the capacity of a RAMDISK, can create the sequence of data with full regard to the priority of each packet, a required frequency of transmission (including a limitation in the
10 number of times a packet or sequence of packets is transmitted), and can precisely control the timing of transmission of each packet, the invention can adjust the timing of packets to suit the ability of receivers to receive and process packets. Instead of the entire
15 content of a RAMDISK being transmitted in a cyclic manner as in the prior art, in the present invention each data packet can have its own cycle and each data packet can have its own lifetime.

Groups of packets can now be cycled in
20 accordance with their own parameters, linking, priority and inter-packet interval. Packets can be synchronized, and can be put into phase, if desired. Patterns of packets can be transmitted which patterns themselves can carry information (a simple example is synchronization
25 to carry timing information for a particular sequence of packets).

In case a small delay is experienced in transmitting a repeatable packet, the system can correct for it in a following transmission. In case a delay is
30 experienced, all of the packets in a sequence can be time shifted to repeat an inter-packet interval, i.e. to correct for the delay.

Because the data is assembled in real time, there is no concern about pages of data as existed in
35 the prior art systems, which had limitations of page

size based on the RAMDISK size, with excess capacity filled with null data. Null data was required to be read to determine that it is indeed null data, which slowed down speed of operation of the system. Null data
5 is not required in the present invention, an emergency data can be inserted into RAM 38 and transmitted immediately without waiting for null data of a RAMDISK to be read at some unknown time, since in the present invention the RAM addresses of data packets to be
10 transmitted can be inserted at any location in table 42, for immediate, timed or repeated transmission.

In addition, because the data packets in the present invention are read designated by pointers from a table, it is a relatively easy task to remove a single
15 packet from a sequence to be transmitted, whereas it was both difficult and time consuming in prior art systems.

Because of transmission of packets in a manner whereby their characteristics define linking of associated packets, timing of transmission and timing
20 and frequency of cycling, and the packets are broadcast to a universe of receivers, the receivers themselves can be dumb terminals, i.e. they can have small, slow or non-existent processing capability. The method described herein therefore provides a capability for the
25 head end system to provide the equivalent of a storage disk or equivalent memory for the terminals, storing data or programs which the terminals can access on an as needed basis as the data or programs are cyclically transmitted.

30 A person skilled in the art having read this specification may now design variations and other embodiments using the principles described herein. All are considered to be within the scope of this invention as defined in the claims appended hereto.

We claim:

1. A method of data assembly for distribution of said data via a mass audience distribution system, comprising:
 - a) storing in a random access memory packets of data which are to be transmitted to the audience,
 - b) storing in a first address table a sequence of memory addresses of said data for transmission of the data in the sequence,
 - c) progressively reading the first table to retrieve the addresses of those packets of data that are to be transmitted to the audience in the sequence,
 - d) reading said memory to retrieve the packets of data in the sequence, and
 - e) transmitting the read packets to the audience in real time.

2. A method as defined in claim 1, including the step of storing a second packet characteristic table which contains a record for each of the packets stored in the memory, each record including a priority field and a repetition period field, reading the second table at least once each interval of time defining a packet, and updating the first table at least once each time the first table is read to specify said memory addresses in the first table in accordance with priority data stored in the priority field and repetition data stored in the repetition field of the second table for each packet of data.

3. A method as defined in claim 2 further including reading incoming data, determining a required priority and repetition frequency, if any, of the incoming data, packetizing the incoming data if it is not packetized, and in which the storing steps include storing packets of

data in the random access memory, updating the second table with data records relating to each of said packets for which priority and repetition frequency were determined, and updating the first table under control of the data stored in
10 the second table.

4. A method as defined in claim 3 including transmitting packets to the audience with an address header and a data load and including the additional step of detecting at least one predetermined address header at a
5 receiver and storing the data load of the packet having the predetermined address header in a local memory.

5. A method as defined in claim 4 including detecting a sequence of packets containing similar, corresponding or associated address headers, retaining data loads of each of the similar, corresponding or associated
5 packets, and assembling the retained data loads into a complete data sequence.

6. A method as defined in claim 3 including modulating the packets of data on a first carrier, and transmitting the modulated carrier with at least one television signal to the audience via a broadcast medium.
5

7. A method as defined in claim 6 in which the modulated carrier is a video carrier, the data being carried during a data channel interval of the television signal.

8. A method as defined in claim 7 in which said data channel interval is during the 21st scanning line of a television picture.

9. A method as defined in claim 6 in which the modulated carrier has a frequency of a separate television channel.

10. A method as defined in claim 3, including attaching an address header to a data load of each packet to be transmitted, modulating different packets of data on plural radio frequency carriers, detecting predetermined address headers at a receiver, and assembling data loads of plural received packets having said predetermined address headers from said plural carriers into a complete data sequence.

11. A method as defined in claim 10 in which the plural carriers are in respective different television channels.

12. A method as defined in claim 1 including transmitting packets to the audience with an address header and a data load and including the additional step of detecting at least one predetermined address header at a receiver and retaining the data load in a local memory.

13. A method as defined in claim 12 including detecting a sequence of packets containing similar address headers, retaining data loads of each of the packets, and assembling the retained data loads into a complete data sequence.

14. A method as defined in claim 1 including modulating the packets of data on a first carrier, and transmitting the modulated carrier with at least one television signal via a broadcast medium.

15. A method as defined in claim 14 in which the modulated carrier is a video carrier, the data being carried during a data channel interval of the television signal.

16. A method as defined in claim 1, including attaching an address header to a data load of each packet to be transmitted, modulating different packets of data on plural radio frequency carriers, detecting predetermined address headers at a receiver, and assembling data loads of plural received packets having said predetermined address headers from said plural carriers into a complete data sequence.

17. A method as defined in claim 16 in which the plural carriers are in respective different television channels.

18. A method as defined in claim 2 in which each record contains, in addition to the priority and repetition fields, additional fields designating linkage of packets on a time frame basis, time spacing of packets, and number of required transmissions of a packet.

19. A method of data distribution comprising storing packets of data in a random access memory, storing a transmission characteristic for each packet, reading said characteristics and transmitting each packet to an audience in accordance with a frequency and timing depending on its particular characteristic.

20. A method of data distribution as defined in claim 19, comprising:

- a) storing in a first table memory addresses of said packets of data,
- b) reading the first table to retrieve the addresses of those packets of data that are to be transmitted to the audience in sequence,
- c) reading said memory to retrieve the packets of data in the sequence, and

10 d) transmitting the read packets to the audience
in real time.

21. A method as defined in claim 20, including
the step of storing a second packet characteristic table
which contains a record for each of the packets stored in
the memory, each record including a priority field and a
5 repetition period field, reading the second table at least
once each packet interval, and updating the first table at
least once each packet interval to specify said memory
addresses in the first table in accordance with priority
data stored in the priority field and repetition data stored
10 in the repetition field for each packet of data.

22. A method as defined in claim 21 in which
both said storing steps are comprised of reading incoming
data, determining a required priority and repetition
frequency, if any, of the incoming data, packetizing the
5 incoming data if it is not packetized, storing packets of
data in the random access memory, updating the second table
with data records relating to each of said packets for which
priority and repetition frequency were determined, and
updating the first table after it has been read.

10 23. A method as defined in claim 19, including
storing in each packet a packet address, storing in each
packet the packet address of a following related packet,
detecting the packet address of a packet at a receiving
5 station, determining from the packet the address of a
following related address, and detecting a particular
following packet by detecting its packet address as
designated by a preceding related address carried in a
preceding packet.

10

24. A method of data distribution comprising storing packets of data in a random access memory, dynamically assembling the stored packets of data into a data stream, transmitting said data stream, and repeating
5 transmission of some of the packets in the data stream in a cyclic manner.

25. A method of data distribution as defined in claim 24 including repeating transmission of others of the packets in further cyclic manner in which further transmission cycles of said others of the packets have
5 different periods and are repeated at different intervals than said some of the packets, cycles of said some of the packets and said others of the packets being dynamically multiplexed.

26. A method as defined in claim 25 in which said packets are read from said memory dynamically in a programmable order to form said cycles.

27. A method as defined in claim 25 in which cycles of some of said others of the packets are nested within a cycle of said some of the packets.

28. A method as defined in claim 25 including synchronizing cycles of several of said others of the packets.

29. A method as defined in claim 25, including transmitting at least one of the cycles of said others of the packets on at least one different channel than a cycle of said some of the packets.

5

30. A method as defined in claim 25, including transmitting of said cycles on a single channel up to the capacity of said single channel, and dynamically transmitting at least one of said cycles of said others of the packets on a different channel in the event of a requirement to transmit packets in excess of the capacity of said single channel.

31. A method as defined in claim 25 including cyclically transmitting a series of cycles of said others of the packets.

32. A method as defined in claim 6 in which the modulated carrier has a frequency of a tunable data channel.

33. A method as defined in claim 24 including modulating the packets of data on a first carrier, and transmitting the modulated carrier to an audience via a broadcast medium.

34. A method as defined in claim 33 in which the modulated carrier has a frequency of a tunable data channel.

35. A method as defined in claim 29, including modulating the cycles of packets for transmission on different carriers for transmission on different tunable data channels, and transmitting the modulated carriers via a broadcast medium.

36. A method as defined in claim 19 in which the transmission characteristic of at least one packet defines immediate transmission, wherein the at least one packet contains an emergency message, and the timing of transmission is immediately after storage of the packet and its characteristic.

37. A method as defined in claim 19, in which the characteristics of a group of packets all relate to parameters of the same service, wherein the frequency and timing of transmission of said packets provide a service
5 which is cycled in accordance with its own parameters.

38. A method as defined in claim 37, in which separate groups of packets have separate, respectively different characteristics, each characteristic relating to a particular service, and transmitting the separate groups of
5 packets at frequencies and timing in accordance with separate characteristics whereby different services are cycled in accordance with their individual different parameters.

39. A method as defined in claim 37, including receiving data and the characteristics of packets to be generated, from at least one of a service provider and a system operator.
5

40. A method as defined in claim 38, in which packets for at least one particular service of said different services are cyclically transmitted for reception at substantially the same time and access by any of a
5 plurality of receivers.

41. A method as defined in claim 19, including receiving said packets at a terminal, and utilizing said packets on an as needed basis by the terminal.

42. A method as defined in claim 19, wherein one of the service providers is a subscriber having a privilege of providing data from which packets may be generated.

43. A method as defined in claim 19, including multiplexing and transmitting multiplexed groups of linked

packets in data channels having bandwidths based on
transmission characteristics of said groups of linked
5 packets.

44. A method as defined in claim 43, wherein said
bandwidths are based on criteria of services provided by
said groups of linked packets.

45. A method as defined in claim 44, in which
said packets for providing said services are transmitted on
at least one logical channel which is distinct from but is
selected from another channel.
5



Application No: GB 9500457.8
Claims searched: 1-18

Examiner: Keith Williams
Date of search: 11 April 1995

Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:
UK CI (Ed.N): H4R(RCC,RCSC,RCT,RCX); H4P(PPEC,PPG,PPS); G4H(HNMC)
Int CI (Ed.6): H04H 1/00; H04L 12/18, 12/56; H04N 7/00, 7/025, 7/03
Other: online WPI

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	GB2208986A Telaction Corp. Fig. 4 & page 23, line 13 onwards	1
A	GB2118750A Telecable Videotron Whole spec. &US4623920	1
A,P	EP0622922A1 2 IBM Corp. abstract Nov 94	1

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
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